Notice of References Cited Application/Control No. | Applicant(s)/Patent Under Reexamination AMOH ET AL. Examiner | Art Unit | Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	Α	US-2004/0004435 A1	01-2004	Hsu, Chi-Hsing	313/512
*	В	US-2004/0026708 A1	02-2004	Chen, Tzer-Perng	257/99
	С	US-			
	D	US-		·	
	Ε	US-			
	F	US			
	G	US-			
	Н	US-			
	1	US-			
	J	US-			
	К	US-			
	L	US-			
	М	US-			-

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	0					,
	Р					
	a					
	R					
	S					•
	Т					-

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)				
	U.	S. Wolf, Silicon Processing for the VLSI Era, 2000, Volume 1, pages 104-107 & 434-437				
-	V	Machine translation of JP-2003-209286, 13 pages				
	w					
	х					

A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)

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